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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

WATANABE, Kenichi

Serial No.: 09/987,012

Filed: November 13, 2001

Group Art Unit: 2815

Examiner: Chris C. Chu

P.T.O. Confirmation No.: 6363

For: SEMICONDUCTOR WAFER DEVICE HAVING SEPARATED CONDUCTIVE  
PATTERNS IN PERIPHERAL AREA AND ITS MANUFACTURING METHOD

**AMENDMENT UNDER 37 CFR §1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 6, 2004

Sir:

In response to the Office Action dated March 15, 2004, extended to August 15, 2004 by a two-month Petition for Extension of Time, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 13 of this paper.